DATA SHEET



SILICON POWER MOS FET NE552R479A

3.0 V OPERATION SILICON RF POWER LDMOS FET FOR 2.45 GHz 0.4 W TRANSMISSION AMPLIFIERS

DESCRIPTION

The NE552R479A is an N-channel silicon power laterally diffused MOS FET specially designed as the transmission power amplifier for 3.0 V WLL products. Dies are manufactured using our NEWMOS2 technology (our WSi gate laterally diffused MOS FET) and housed in a surface mount package. This device can deliver 26.0 dBm output power with 45% power added efficiency at 2.45 GHz under the 3.0 V supply voltage.

FEATURES

High output power : Pout = 26.0 dBm TYP. (VDs = 3.0 V, IDset = 200 mA, f = 2.45 GHz, Pin = 19 dBm)
 High power added efficiency : ηadd = 45% TYP. (VDs = 3.0 V, IDset = 200 mA, f = 2.45 GHz, Pin = 19 dBm)
 High linear gain : GL = 11 dB TYP. (VDs = 3.0 V, IDset = 200 mA, f = 2.45 GHz, Pin = 10 dBm)

• Surface mount package : $5.7 \times 5.7 \times 1.1$ mm MAX.

• Single supply : V_{DS} = 2.8 to 6.0 V

APPLICATIONS

Digital cellular phones
 Analog cellular phones
 Bluetooth[™] applications
 Others
 3.0 V GSM1900 Pre Driver
 2.8 V AMPS Handsets
 3.0 V Class 1 Devices
 3.0 V Two-Way Pagers

ORDERING INFORMATION

| Part Number | Package | Marking | Supplying Form |
|----------------|---------|---------|---|
| NE552R479A-T1 | 79A | AW | 12 mm wide embossed tapingGate pin face the perforation side of the tapeQty 1 kpcs/reel |
| NE552R479A-T1A | | | 12 mm wide embossed tapingGate pin face the perforation side of the tapeQty 5 kpcs/reel |

Remark To order evaluation samples, contact your nearby sales office.

Part number for sample order: NE552R479A-A

Caution: Observe precautions when handling because these devices are sensitive to electrostatic discharge

The information in this document is subject to change without notice. Before using this document, please confirm that this is the latest version.

ABSOLUTE MAXIMUM RATINGS ($T_A = +25$ °C)

| Parameter | Symbol | Ratings | Unit |
|----------------------------|------------------|-------------|------|
| Drain to Source Voltage | Vos | 15.0 | V |
| Gate to Source Voltage | Vgs | 5.0 | V |
| Drain Current | lο | 300 | mA |
| Drain Current (Pulse Test) | ID Note | 600 | mA |
| Total Power Dissipation | Ptot | 10 | W |
| Channel Temperature | Tch | 125 | °C |
| Storage Temperature | T _{stg} | -55 to +125 | °C |

Note Duty Cycle 50%, $T_{on} \le 1 \text{ s}$

RECOMMENDED OPERATING CONDITIONS

| Parameter | Symbol | Test Conditions | MIN. | TYP. | MAX. | Unit |
|-------------------------|--------|---------------------------------------|------|------|------|------|
| Drain to Source Voltage | VDS | | 2.8 | 3.0 | 6.0 | V |
| Gate to Source Voltage | Vgs | | 0 | 2.0 | 3.0 | V |
| Drain Current | ΙD | Duty Cycle 50%, Ton ≤ 1 s | . 1 | 200 | 500 | mA |
| Input Power | Pin | f = 2.45 GHz, V _{DS} = 3.0 V | 18 | 19 | 25 | dBm |

ELECTRICAL CHARACTERISTICS

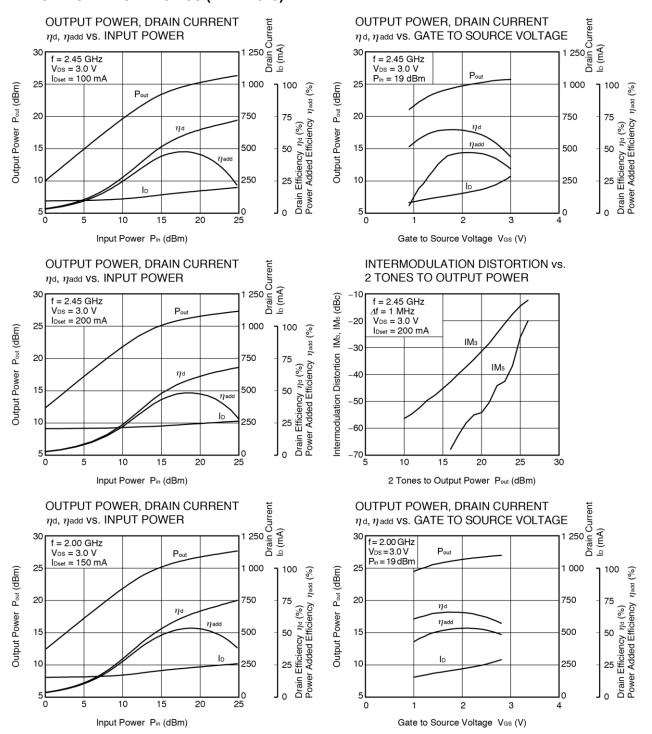
(T_A = +25°C, unless otherwise specified, using NEC standard test fixture)

| Parameter | Symbol | Test Conditions | MIN. | TYP. | MAX. | Unit |
|---|------------|---|------|------|------|------|
| Gate to Source Leak Current | Igss | Vgs = 5.0 V | _ | _ | 100 | nA |
| Drain to Source Leakage Current (Zero Gate Voltage Drain Current) | loss | V _{DS} = 6.0 V | - | - | 100 | nA |
| Gate Threshold Voltage | Vth | V _{DS} = 3.5 V, I _D = 1 mA | 1.0 | 1.4 | 1.9 | ٧ |
| Thermal Resistance | Rth | Channel to Case | - | - | 10 | °C/W |
| Transconductance | Gm | V _{DS} = 3.5 V, I _D = 100 mA | _ | 0.4 | - | S |
| Drain to Source Breakdown Voltage | BVDSS | loss = 10 μ A | 15 | 18 | - | V |
| Output Power | Pout | f = 2.45 GHz, Vps = 3.0 V, | 24.0 | 26.0 | - | dBm |
| Drain Current | ΙD | P _{in} = 19 dBm, | = | 230 | - | mA |
| Power Added Efficiency | η add | I _{Dset} = 200 mA (RF OFF), Note1 | 35 | 45 | - | % |
| Linear Gain Note2 | G∟ | | - | 11 | _ | dB |

Notes 1. DC performance is 100% testing. RF performance is testing several samples per wafer. Wafer rejection criteria for standard devices is 1 reject for several samples.

2. Pin = 10 dBm

TYPICAL CHARACTERISTICS (TA = +25°C)



Remark The graphs indicate nominal characteristics.

S-PARAMETERS

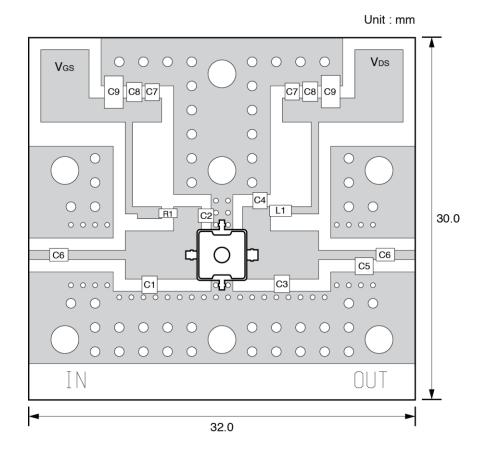
- S-parameters and noise parameters are provided on our Web site in a format (S2P) that enables the direct import of the parameters to microwave circuit simulators without the need for keyboard inputs.
- · Click here to download S-parameters.
- [RF and Microwave] ® [Device Parameters]
- URL http://www.necel.com/microwave/en/

LARGE SIGNAL IMPEDANCE (VDS = 3.0 V, ID = 200 mA, f = 2.45 GHz, Pout = 400 mW)

| f (GHz) | $Z_{in}\left(\Omega \right)$ | $ZoL\left(\Omega\right)^{Note}$ | | |
|---------|-------------------------------|---------------------------------|--|--|
| 2.45 | 2.96 -j7.78 | 3.36 -j8.42 | | |

Note ZoL is the conjugate of optimum load impedance at given voltage, idling current, input power and frequency.

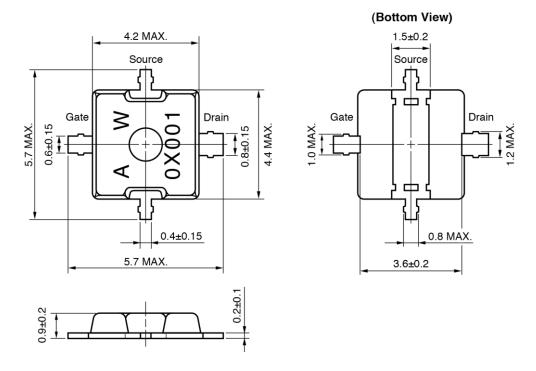
■ EVALUATION BOARD FOR 2.45 GHz



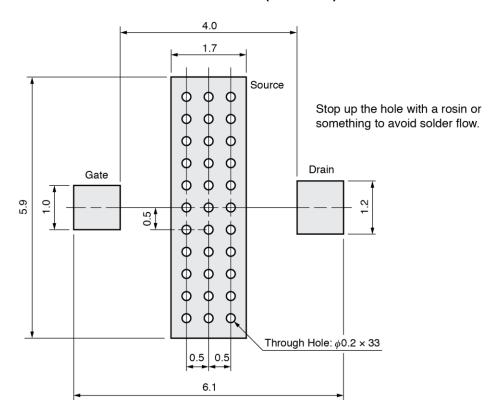
| Symbol | Value | Comment |
|---------------|--|---------|
| C1 | 2.0 pF | |
| C2 | 1.4 pF | |
| C3 | 2.2 pF | |
| C4 | 0.8 pF | |
| C5 | 0.5 pF | |
| C6 | 10 pF | |
| C7 | 1 000 pF | |
| C8 | 0.22 μF | |
| C9 | 3.3 μF - 16V | |
| R1 | 1 000 Ω | |
| L1 | 22 nH | |
| Circuit Board | $t = 0.4 \text{ mm}, \ \epsilon \ r = 4.5$ | R4775 |

PACKAGE DIMENSIONS

79A (UNIT: mm)



79A PACKAGE RECOMMENDED P.C.B. LAYOUT (UNIT: mm)



RECOMMENDED SOLDERING CONDITIONS

This product should be soldered and mounted under the following recommended conditions. For soldering methods and conditions other than those recommended below, contact your nearby sales office.

| Soldering Method | Soldering Conditions | Condition Symbol | |
|------------------|---|---|----------|
| Infrared Reflow | Peak temperature (package surface temperature) Time at peak temperature Time at temperature of 220°C or higher Preheating time at 120 to 180°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass) | : 260°C or below : 10 seconds or less : 60 seconds or less : 120±30 seconds : 3 times : 0.2%(Wt.) or below | IR260 |
| VPS | Peak temperature (package surface temperature) Time at temperature of 200°C or higher Preheating time at 120 to 150°C Maximum number of reflow processes Maximum chlorine content of rosin flux (% mass) | : 215°C or below : 25 to 40 seconds : 30 to 60 seconds : 3 times : 0.2%(Wt.) or below | VP215 |
| Wave Soldering | Peak temperature (molten solder temperature) Time at peak temperature Preheating temperature (package surface temperature) Maximum number of flow processes Maximum chlorine content of rosin flux (% mass) | : 260°C or below : 10 seconds or less : 120°C or below : 1 time : 0.2%(Wt.) or below | WS260 |
| Partial Heating | Peak temperature (pin temperature) Soldering time (per pin of device) Maximum chlorine content of rosin flux (% mass) | : 350°C or below : 3 seconds or less : 0.2%(Wt.) or below | HS350-P3 |

Caution Do not use different soldering methods together (except for partial heating).